



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Low, *et al.*Docket No.: 02 P 15173 US / INTECH
3.0-076

Serial No.: 10/689,233

Art Unit: 2891

Filed: October 20, 2003

Examiner: Asok K. Sarkar

For: Inclusion Of Low-K Dielectric Material Between Bit Lines

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO NOTICE OF DRAWING INCONSISTENCY
WITH SPECIFICATION

Dear Sir:

The following amendments and remarks are presented in response to the Examiner's Notice of Drawing Inconsistency with Specification mailed May 10, 2006. Please amend the above-referenced application as follows.